A Grid-Connected Photovoltaic Interface System for Delivering Active and Reactive Powers

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ABSTRACT
This paper presents a grid-connected photovoltaic (PV) interface for delivering both active and reactive powers. The PV interface employs H-bridge topology DC-DC converter and inverter with analog control technology. The power flow is controlled solely by the adjustable DC output voltage of the DC to DC converter. In order to evaluate the PV interface system’s performances, it is tested by delivering power to the grid with low power factor. The experimental results show that at 300W active load, the inverter could deliver the reactive power of 400VAR. The PV interface could also produce very low harmonic voltage and current distortions. The laboratory measurements show that the total harmonic distortions of inverter output voltage and current are 0.46% and 0.05%, respectively.

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1. INTRODUCTION
As the rapid progress of power electronics technology, the photovoltaic (PV) energy is now well-known as an important energy resource, because it is renewable, clean, abundant, and no pollution produced. The PV system becomes more interesting and the installed capacity has rapidly increased around the world [1].

The PV system could be operated in grid-connected and off-grid mode of operations. In a grid-connected PV system, the PV is commonly interfacing the grid through a voltage source inverter [2]. The major concerns of the PV interface are the efficiency and controllability, including the power quality [3], [4], when it is connected to the grid which not only absorbs active power, but also reactive power. The inverter of PV interface has to be able to operate in reactive power mode, instead of in active power mode [5]. Many control methods [6]-[9] have been investigated to deal with the problems in order to improve the PV interface’s performances. However, the good PV interface needs very complex and relative expensive controller.

In this paper, an alternative simple and low cost grid-connected PV interface system is proposed to delivery both active and reactive powers. The whole control system is implemented using analog control technology. The interface treated here is designed on the basis of computer simulation and laboratory experiments are conducted to evaluate its performances. The PV interface is tested to deliver power to the grid with a low power factor.
2. RESEARCH METHOD

The grid-connected PV interface system consists of PV array, interface, and grid as seen in Figure 1. The interface circuit consists of two sections: a DC to DC converter and an inverter. The DC to DC converter is used to convert the DC input voltage to a proper level of DC output voltage. The DC output voltage is then fed to the inverter. The inverter is used to convert the DC input voltage to AC output voltage at proper magnitude, frequency and phase angle, before it is interconnected with the utility grid.

The DC to DC converter employs an H-bridge topology and a high frequency ferrite transformer, as shown in Figure 2. The isolation is incorporated for the flexibility and safety purpose. For the simplicity purpose, the two-level operation mode is applied. The DC output voltage must be set to a proper level so that the amount of power delivered is as expected. For a stand-alone PV system, a high DC voltage is a must for feeding the load via transformerless inverter and this leads to the need of a high step up DC to DC converter [10], [11].

A predetermined dead-time is applied to avoid overlapped operation of the switches of the same legs, which may lead to switches damage. The dead-time and a commutating capacitors are also needed to perform soft-switched operation of the switches. The pulses wide modulation controller is driven by a microcontroller so that the switches (i.e. MOSFETS) are easily driven due to its chip’s high current sourcing capability. The four switchers are driven separately and isolated each other by using auto-coupler pulse transformers. A PI-compensator is used to stabilize the DC output voltage. A tolerable overshoot occasionally occurs due to the output LC filter property.

Referring to Figure 3, the operation principle of power delivering from the inverter to the grid can be explained as follows. The fundamental component of inverter output voltage can be written as $|V_{\text{inv}}| \angle \delta$, while the grid voltage can be written as $|V_{\text{grid}}| \angle 0$. Then, AC current flowing from the inverter output to the grid is expressed by

$$I = \frac{|V_{\text{inv}}|}{|Z|} \angle (\delta - \gamma) - \frac{|V_{\text{grid}}|}{|Z|} \angle - \gamma \tag{1}$$
The complex power delivered by the inverter is then given by

$$ S = \frac{V_{inv}}{Z_i} \cos \gamma - \frac{V_{inv} |V_{grid}|}{Z_i} \cos(\gamma + \delta) $$

(2)

The active and reactive powers are

$$ P = \frac{V_{inv}}{Z_i} \cos \gamma - \frac{V_{inv} |V_{grid}|}{Z_i} \cos(\gamma + \delta) $$

(3)

$$ Q = \frac{V_{inv}}{Z_i} \sin \gamma - \frac{V_{inv} |V_{grid}|}{Z_i} \sin(\gamma + \delta) $$

(4)

The tie-line reactor could be assumed as a purely inductive or $\gamma = 90^\circ$, and hence

$$ P = \frac{V_{inv} |V_{grid}|}{X_i} \sin \delta $$

(5)

$$ Q = \frac{V_{inv}}{X_i} - \frac{V_{inv} |V_{grid}|}{X_i} \cos \delta $$

(6)

In this work, the inverter output voltage ($V_{inv}$) is generated by an H-bridge topology as shown in Figure 4. The inverter is operated at three-level mode and sinewave modulation at fixed modulation index of 1.0.
From (5) and (6), it is shown that the active and reactive powers delivered by the inverter are dependent to the inverter voltage and phase angle, while tie-line reactance and grid voltage are set as parameters. To achieve the maximum active power delivered, the phase angle must be set to 90°. At this angle, the inverter sends the reactive power at the value of $|V_{inv}|^2/X_t$. Although the active and reactive power flow could be controlled by varying the phase angle $\delta$, this technique is not used due to the nonlinearity property of the function, as mentioned in (5) and (6). In this case, the power flow is controlled solely by the DC bus voltage. In this work, the phase shifting of 90° is implemented by using a small-signal operational amplifier as seen in Figure 5.

Due to the dead-time effect, there is a possibility of the inverter to produce low-order harmonic voltage, which could be omitted. The voltage harmonics only give small effect on current harmonics due to the current filtering action of the tie-line reactor. This filtering effect also enables to use low frequency switching for the inverter. Low frequency switching implies low switching losses. The prototype is run by switching frequency of 1 kHz.

3. RESULTS AND ANALYSIS

In this section, computer simulations and laboratory experimental analyses are conducted to analyze the proposed grid-connected PV system’s performances. The grid-connected PV circuit and the prototype are presented in Figures 6 and 7. The grid-connected PV system is designed to deliver 300W active power and 400VAR reactive power, and hence it is needed the 500VA rating of inverter.

The circuit variables and parameters are: $V_{in} = 36$ VDC, voltage ratio of transformer is $N_p:N_s = 9:108$ with $L_m = 1.5\text{mH}$, inductor as interface between inverter and grid utility $L_F = 1.0\text{mH}$. The capacitor $C_F = 330 \ \mu\text{F}$, capacitors $C_1, C_2, C_3, C_4$ are 22nF and $L_t = 0.81\text{mH}$. The delay time of MOSFET- IRFP450...
is \( t_d = 3.75 \) s and switching frequency of switches is \( f_s = 16 \) kHz with modulation index \( m = 1.0 \). The phase angle is set on \( \delta = 90^\circ \), grid voltage \( V_{\text{grid}} = 220V \) and system frequency \( f = 50Hz \).

Simulation and experimental results show that the waveform of inverter output voltage, grid voltage and inverter output current as seen in Figures 8-10. It is proven that the phase shifter works perfectly and gives filtering effect as seen in Figure 11. Although the grid voltage is a few distorted, the shifted signal has a better waveform. The gate drivers use low-cost CMOS 4050s and 6N136 opto-isolators. The phase displacement of the inverter fundamental output voltage is advanced by 90º. The total harmonic distortion (THD) of inverter output voltage and current are 0.46% and 0.05%, respectively. The total harmonic current distortion is less than 5%, and it is inside the limit as specified by IEC 61727 for utility interface [12].

![Prototype of grid-connected PV system interface](image1)

Figure 7. Prototype of grid-connected PV system interface

![Inverter output voltage, grid voltages, and inverter output current](image2)

Figure 8. Inverter output voltage, grid voltages, and inverter output current
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From the experimental results, it is also found that at 300W active load, the output inverter voltage $V_{inv}$ is 325V while grid voltage is 240V. At this condition, the inverter delivers the reactive power of 400VAR. Hence, the apparent power sent by the inverter is 500VA. Increasing the reactive power delivered to the grid increases the apparent power but it does not have impact on the active power. However, the PV interface has to supply more apparent power if the grid needs larger reactive power.

4. CONCLUSION

A simple and low-cost grid-connected PV interface system has been designed and tested in the laboratory. It has been proven that the PV interface has the capability to produce both active and reactive powers with low harmonic output voltage and current distortions. The whole system is implemented using commercial standard components. For this system presented, the transmission of reactive power is inevitable. The filtering effect of the tie-line reactance enables to use low switching-frequency. This advantage implies to high efficiency for the inverter section.

REFERENCES


